

	***	HDNG	1130 CORE IMAGE LOADER-CARD	CIL 000
	*			CIL00010
	*		CORE IMAGE LOADER	CIL00010
		ABS		CIL00020
02EE		ORG	0	CIL00040
	*			CIL00050
	*			CIL00060
0000		ZERO	EQU 0	CIL00070
0000 0		TEMP	MDX START	CIL00080
0001 0		MOVE	DC /1004	CIL00090
0002 0		MASK	DC /0000	CIL00100
0003 0			DC /0020	CIL00110
0004 0		READ	DC CHECK	CIL00120
0005 0			DC /0000	CIL00130
0006 0		START	LD MASK+1	CIL00140
0007 0			SLA 2	CIL00150
0008 0			STO MASK+1	CIL00160
0009 0			SLA 2	CIL00170
000A 0			STO TEMP	CIL00180
000B 0			A SETUP	CIL00190
000C 0			STO SETUP	CIL00200
000D 0			A CHECK	CIL00210
000E 0			STO CHECK	CIL00220
000F 0			S AB+1	CIL00230
0010 0			STO AB+1	CIL00240
0011 0			A LOOP	CIL00250
0012 0			STO LOOP	CIL00260
0013 0			A AB	CIL00270
0014 0			A AB	CIL00280
0015 0			STO AB	CIL00290
0016 0			LD TEMP	CIL00300
0017 0			A MOVE	CIL00310
0018 0			STO READ+1	CIL00320
0019 0			A TEMP	CIL00330
001A 0			A MASK+1	CIL00340
001B 0			S MOVE	CIL00350
001C 0			STO MASK+1	CIL00360
001D 0			SRA 1	CIL00370
001E 0			A TEMP	CIL00380
001F 0			A MOVE	CIL00390
0020 0			STO MOVE	CIL00400
0021 0			A SENSE	CIL00410
0022 0			A TEMP	CIL00420
0023 0			STO SENSE	CIL00430
0024 0			LD MASK	CIL00440
0025 0			RTE 27	CIL00450
0026 0			SLA 4	CIL00460
0027 0			RTE 5	CIL00470
0028 0			STO MASK	CIL00480
0029 0		SETUP	LDX 31	CIL00490
002A 0			LD FIXIT	CIL00500
002B 0		LOOP	DC /6006	CIL00510
002C 0		CHECK	DC /1000	CIL00520
002D 0			MDX LOOP	CIL00530
002E 0			XIO MASK	CIL00540
002F 0		CARD	XIO MOVE-1	CIL00550
0030 0		WAIT	MDX WAIT	CIL00560
0031		INTER	BSS 1	CIL00570

A REGISTER BELOW

0000	0000	0010	0000	
0000	0000	1000	0000	
0000	0000	1000	0000	=MASK+1
0000	0010	0000	0000	=TEMP
0110	0010	0001	1111	
0110	0010	0001	1111	=SETUP LD
0111	0010	0111	1111	=CHECK MD
0111	0010	0000	0001	
0111	0010	0000	0001	=AB+1 MD
		1101	0010	0000 0111
		1101	0010	0000 0111
		1101	0010	0010 1011
		1101	0010	0100 1111
0000	0010	0000	0000	
	0001	0010	0000	0100
	0001	0010	0000	0100
	0001	0100	0000	0100
	0001	0100	1000	0100
0000	0100	1000	0000	=MASK+1
0000	0010	0100	0000	
0000	0100	0100	0000	
	0001	0100	0100	0100
	0001	0101	0000	0111
	0001	0111	0000	0111
	0001	0111	0000	0111

LOADS INDEX 2 WITH 31  
LOAD INTER.ROUTINES ADDR.  
= STO 2 7 PLACE IRA IN WDS  
=MDX 2 -1 THEN SKIPS OVER BRN  
BRNCH BACK TO LOOP  
ALLOWS INTER.LEVELS 0-15  
STARTS CARD MOVING  
WAIT FOR INTER.,ALSO USED AS  
INTER. PROCESSING ROUTINE

[illegible]

END OF INPUT BUFFER

[illegible]

\* THIS ROUTINE SERVICES COLUMN INTERRUPTS AND

\* PACKS 4 COLUMNS INTO 3 WORDS.  
\* IT FILLS THE BUFFER CALLED INPUT  
\*

0095 0 08FA  
0096 0 D0D9  
0097 00 C4000032  
0099 0 18D0  
009A 0 C01A  
009B 0 18D0  
009C 00 D4800098  
009E 0 18D0

COLM1 XIO RED  
STO CITST SET COL. INT. TEST IND.  
LOOP1 LD L INPUT-1  
RTE 16  
LD CK1  
GOB RTE 16  
STO I LOOP1+1  
RTE 16

\* DATA IN CARD 4 STARTS IN 009F  
\*  
\*

009F 00 D4000033  
00A1 00 74FC009B  
00A3 0 C0F7  
00A4 0 F009  
00A5 00 4C1800B0  
00A7 00 74010098  
00A9 00 740100A0  
00AB 00 74040099  
00AD 0 4878  
00AE 0 18C0  
00AF 0 70CE  
00B0 00 74100098  
00B2 00 74F40099  
00B4 0 70F8

STO L INPUT  
MDX L GOB,-4  
LD GOB  
EOR SENS1  
BSC L NEW,+  
MDX L LOOP1+1,1  
MDX L GOB+5,1  
MDX L GOB-2,4  
WAIT1 BOSC +-Z  
SENS1 DC /18C0  
MDX WAIT3 WAIT FOR NEXT INT  
NEW MDX L GOB,16  
MDX L GOB-2,-12  
MDX WAIT1

\* STORES BUFFER TO CORE  
\*

00B5 0 0000  
00B6 00 C4000035  
00B8 0 E00B  
00B9 0 D001  
00BA 00 66000000  
00BC 00 C4000033  
00BE 0 9015  
00BF 0 D003  
00C0 00 C6000035  
00C2 00 D6000000  
00C4 0 72FF  
00C5 0 70FA

CK1 DC 0 COLUMN BUFFER (FROM CARD)  
DAT LD L INPUT+2 GET TYPE AND COUNT  
AND N REMOVE COUNT  
STO LAB+1 STORE COUNT IN NEXT INST.  
LAB LDX L2 \*-\* LOAD IR2 WITH COUNT  
LD L INPUT GET LOADING ADDRESS  
S ONE  
STO LOOP3+3 SET UP DATA STORE INSTR.  
LOOP3 LD L2 INPUT+2 GET DATA WORD  
STO L2 \*-\* STORE DATA WORD  
N MDX 2 -1 DECK IR2 BY 1, IF COUNT=0 SK  
MDX LOOP3 CONTINUE LOADING FROM INPUT

\* DATA IN CARD 5 STARTS IN 00C7  
\*  
\*

00C6 00 C4000035  
00C8 00 4C500004  
00CA 00 67800067  
00CC 0 0805  
00CE 0 1C03  
00CF 0 4828  
00D0 00 4CC00068  
00D2 0 18D0

LD L INPUT+2 GET TYPE  
BOSC L BSIXX,- END-OF-PROGRAM TEST  
EOP LDX I3 INPUT+52 YES - SET IR-3 TO TV ADDR.  
XIO SCRP2-1 LAST CARD TEST  
SLA 3  
BSC +Z  
XIQ CFEEED-1 YES - FEED OUT  
BOSC I INPUT+53 GO TO EXECUTE  
RTEXX RTE 16 CONSTANT FOR REST. RTE

CIL01760  
CIL01770  
CIL01780  
CIL01790  
CIL01800  
CIL01810  
CIL01820  
CIL01830  
CIL01840  
CIL01850  
CIL01860  
CIL01870  
CIL01880  
CIL01890  
CIL01900  
CIL01910  
CIL01920  
CIL01930  
CIL01940  
CIL01950  
CIL01960  
CIL01970  
CIL01980  
CIL01990  
CIL02000  
CIL02010  
CIL02020  
CIL02030  
CIL02040  
CIL02050  
CIL02060  
CIL02070  
CIL02080  
CIL02090  
CIL02100  
CIL02110  
CIL02120  
CIL02130  
CIL02140  
CIL02150  
CIL02160  
CIL02170  
CIL02180  
CIL02190  
CIL02200  
CIL02210  
CIL02220  
CIL02230  
CIL02240  
CIL02250  
CIL02260  
CIL02270  
CIL02280  
CIL02290  
CIL02300  
CIL02310  
CIL02320  
CIL02330  
CIL02340

0003 0 1703  
0004 0 0001  
0005 0 709D  
0006

SCRIP2 DC /1703 IOCC TO SENSE DSW  
ONE DC /0001  
MDX CD  
ORG /4

\*  
\* THIS SECTION WAS LOADED INTO CORE INITIALLY  
\* AT LOC. /0006. IT IS MOVED TO PROPER ADDRESS  
\* DURING EXECUTION OF PROGRAM LABELED CON.  
\* THIS CODING PROCESSES CHECK SUMS, HANDLES  
\* PROGRAM STOP, ACCOMODATES ERRORS, AND  
\* SETS UP THE INTERRUPT TV.  
\*

0004 0 4001  
0005 0 706D  
0006 0 0000  
0007 C 7006  
0008 0 0080  
0009 0 0000  
000A 0 0000  
000B 0 3F00  
000C 0 0080  
000D 0 0006  
000E 0 00F8  
000F C 08FA  
0010 0 4828  
0011 0 3000  
0012 0 00F7  
0013 00 4CC00006  
0015 0 001E  
0016 00 4C1800B6  
0018 00 74010026  
001A 0 6236  
001B 0 000A  
001C 00 86000032  
001E 0 4802  
001F 0 8007  
0020 0 72FF

BSIXX BSI LEV5 SET RETURN ADDRESS  
MDX CD  
LEV5 DC 0 LEVEL 5 ENTRY  
MDX \*+6 CONTINUE LEVEL 5  
DC INT  
DC 0  
IOCCX DC 0 IOCC TO SENSE  
DC /3F00 CONSOLE STATUS  
DC INT  
DC LEV5  
STO IOCCX SAVE ACCUMULATOR  
XIO IOCCX SENSE CONSOLE STATUS  
BSC Z+ TEST FOR PROGRAM STOP  
WAIT YES - STOP  
LD IOCCX RESTORE ACCUMULATOR  
BOSC I LEV5 RESTART NEW CARD  
CKSUM LD INPUT+1 TEST CARD CHECK SUM  
BSC L DAT,+ IF ZERO, LOAD IMMEDIATELY  
MDX L CDCNT,1 INCR CARD COUNT BY ONE  
LDX 2 54  
LD CDCNT  
OTTO A L2 INPUT-1 ADD CARD COUNT PLUS  
BSC C 54 WORDS (72 COLUMNS)  
A ONE4 IF CARRY,  
MDX 2 -1 ADD ONE

\*  
\* DATA IN CARD 6 STARTS IN /00EF  
\*

0021 0 70FA  
0022 0 9C04  
0023 00 4C1800B6  
0025 0 3000  
0026 0 0000  
0027 0 0001  
0028 00 4C180015  
002A 0 0045  
002B 00 4C200030  
002D 00 74FF0026  
002F 0 1000  
0030 0 1810  
0031 0 7C60

MDX OTTO  
S ONE4 MAKE TWOS COMPLEMENT  
BSC L DAT,+ TEST FOR ZERO  
CK3 WAIT NO - CHECK SUM ERROR  
CDCNT DC 0  
ONE4 DC 1  
FDCHK BSC L CKSUM,+ BRANCH IF NO READER ERROR  
LD CITST GET COL. INT. INDICATOR  
BSC L \*+3,Z TEST FOR ANY COL. INTS.  
MDX L CDCNT,-1 NO, REDUCE CARD COUNT FOR  
NOP RE-READ OF CARD IN FEED  
SRA 16 YES, CLEAR ACCUM.  
MDX ERSTP

0033  
0032 0 0000  
NO ERRORS

\*  
\* INPUT EQU 51 INPUT BUFFER ADDRESS  
END TEMP

CIL0235  
CIL0236  
CIL0237  
CIL0238  
CIL0239  
CIL0240  
CIL0241  
CIL0242  
CIL0243  
CIL0244  
CIL0245  
CIL0246  
CIL0247  
CIL0248  
CIL0249  
CIL0250  
CIL0251  
CIL0252  
CIL0253  
CIL0254  
CIL0255  
CIL0256  
CIL0257  
CIL0258  
CIL0259  
CIL0260  
CIL0261  
CIL0262  
CIL0263  
CIL0264  
CIL0265  
CIL0266  
CIL0267  
CIL0268  
CIL0269  
CIL0270  
CIL0271  
CIL0272  
CIL0273  
CIL0274  
CIL0275  
CIL0276  
CIL0277  
CIL0278  
CIL0279  
CIL0280  
CIL0281  
CIL0282  
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CIL0291